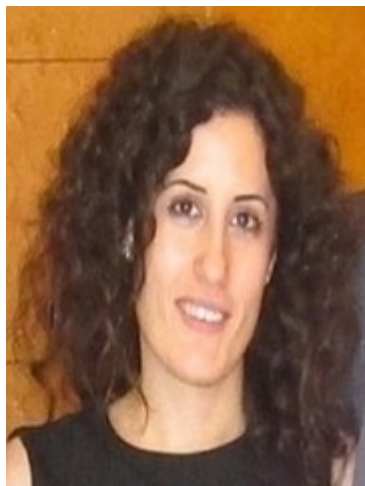




## **Prof. Aditya K. Jagannatham**

Prof. Aditya K. Jagannatham received his Bachelors degree from the Indian Institute of Technology, Bombay and M.S. and Ph.D. degrees from the University of California, San Diego, U.S.A.. From April '07 to May '09 he was employed as a senior wireless systems engineer at Qualcomm Inc., San Diego, California, where he worked on developing 3G UMTS/WCDMA/HSDPA mobile chipsets as part of the Qualcomm CDMA technologies division. His research interests are in the area of next-generation wireless communications and networking, sensor and ad-hoc networks, digital video processing for wireless systems, wireless 3G/4G cellular standards and CDMA/OFDM/MIMO wireless technologies. He has contributed to the 802.11n high throughput wireless LAN standard and has published extensively in leading international journals and conferences. He was awarded the CAL(IT)<sup>2</sup> fellowship for pursuing graduate studies at the University of California San Diego and in 2009 he received the Upendra Patel Achievement Award for his efforts towards developing HSDPA/HSUPA/HSPA+ WCDMA technologies at Qualcomm. Since 2009 he has been a faculty member in the Electrical Engineering department at IIT Kanpur, where he is currently a Professor, and is also associated with the BSNL-IITK Telecom Center of Excellence (BITCOE). At IIT Kanpur he has been twice awarded the P.K. Kelkar Young Faculty Research Fellowship (June 2012 to May 2015), (June 2015 to May 2018) for excellence in research and the Qualcomm Innovation Fellowship (QInF). His popular video lectures for the NPTEL (National Programme on Technology Enhanced Learning) course on Advanced 3G and 4G Wireless Mobile Communications.



## **Nancy Dib**

Nancy Dib is the Head of Academic Marketing | Go-To-Market Lead, EMEA at National Instruments, Lebanon. She received her Bachelors degree from electrical and mechanical engineering, from the Saint Joseph University of Beirut, and her Masters degree in Electrical and Computer Engineering from the American University of Beirut. She has over 9 years of Working Experience at National Instruments.



## **Prof. Dr. Saad Mekhilef**

Prof. Dr. Saad Mekhilef is an IET Fellow and IEEE senior member. He is the associate editor of IEEE Transaction on Power Electronics and Journal of Power Electronics. He is currently a Professor in the Department of Electrical Engineering, University of Malaya. He is the Director of Power Electronics and Renewable Energy Research Laboratory. He is the author and co-author of more than 250 publications in international journals and proceedings (189 ISI journal papers) with more than 11500 citations and 54 H-index, 92 Ph.D. and master students have graduated under his supervision. He is actively involved in industrial consultancy for major corporations in the power electronics projects. His research interests include power conversion techniques, control of power converters, renewable energy, and energy efficiency.



## **Dr.SureshShanmugasundaram**

Dr. Suresh has been serving as academician and researcher for over a decade and is currently working as Professor & Dean, Faculty of Engineering & Applied Sciences at Botho University, Botswana. He graduated his Bachelors of Engineering (Computer Science & Engineering) from Bharathiar University, India. Then he graduated his Master of Science in Computer Networks from Middlesex University, London. His Master Degree thesis was studies on an Efficient Approach for Secured Group Key Agreement. Later, he obtained his PhD from Anna University, Chennai, India. He is a certified Associate Lecturer for The Open University, Milton Keynes, UK. Dr. Suresh has authored a book on 'Grid Computing Framework: Income Administration & Security', published at Germany. His areas of interest include Grid Computing, Cloud Computing, Hive Computing, High Performance Computer Networks and Distributed Computing. He has also authored and presented several research papers in various scientific forums. He is a member of the Technical Review Committee for International Association of Science and Technology for Development since 2008. He has represented various International Conferences as International Technical Committee Member and chaired several scientific forums. Countries visited by him include United Kingdom, South Africa, Namibia, Zambia, Thailand, Singapore, UAE, Kenya, Mauritius etc. He has a vast experience in lecturing and tutoring the modules like Object Oriented Analysis and Design, Programming in C, Computer Networks, MIS, ERP, Software Engineering Methodologies, Network Security and Distributed Computing both for UG and PG Engineering students. He has adjudicated various PhD thesis from various Universities as their External Examiner. Owing to the accolades he had gained during these years, Dr. Suresh has been nominated for Marquis Who's Who in the World 2015. Since 1899, Marquis Who's Who has remained the standard for reliable and comprehensive biographical data of eminent personalities.



## Mr. R. Venkatraman

Venkatraman is a Senior Member Technical Staff Texas Instruments, Bangalore, currently working as Senior Technologist in the Connected MCU Design team. He has over 18 years of experience in the design and design methodology definition of DSPs and MCUs. He completed his BE (ECE) from College of Engineering, Guindy, Chennai and his ME (Microelectronics) from Indian Institute of Science, Bangalore. He holds 3 patents and has over 50 publications in various international conferences, symposia or EDA design user groups.

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